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INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 4024-4026PC	FOR FURTHER ACTION See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416)	
International application No. PCT/US02/22096	International filing date (day/month/year) 28 June 2002 (28.06.2002)	Priority date (day/month/year) 29 June 2001 (29.06.2001)
International Patent Classification (IPC) or national classification and IPC IPC(7): H03F 1/02, 21/00, 3/45, 3/16; H03I 5/00 and US Cl.: 330/9, 11, 252, 253, 257, 300; 327/307		
Applicant XANOPTIX, INC.		

<ol style="list-style-type: none"> This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36. This REPORT consists of a total of <u>3</u> sheets, including this cover sheet. <input type="checkbox"/> This report is also accompanied by ANNEXES, i.e., sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT). These annexes consist of a total of ___ sheets.
<ol style="list-style-type: none"> This report contains indications relating to the following items: <ul style="list-style-type: none"> I <input checked="" type="checkbox"/> Basis of the report II <input type="checkbox"/> Priority III <input type="checkbox"/> Non-establishment of report with regard to novelty, inventive step and industrial applicability IV <input type="checkbox"/> Lack of unity of invention V <input checked="" type="checkbox"/> Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement VI <input type="checkbox"/> Certain documents cited VII <input type="checkbox"/> Certain defects in the international application VIII <input type="checkbox"/> Certain observations on the international application

Date of submission of the demand 27 January 2003 (27.01.2003)	Date of completion of this report 16 September 2004 (16.09.2004)
Name and mailing address of the IPEA/US Mail Stop PCT, Attn: IPEA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 Facsimile No. (703) 305-3230	Authorized officer Patricia Nguyen  Telephone No. (571) 272-1768

I. Basis of the report**1. With regard to the elements of the international application:***

the international application as originally filed.

the description:

pages 1-4 as originally filed

pages NONE, filed with the demand

pages NONE, filed with the letter of _____.

the claims:

pages 5 and 6, as originally filed

pages NONE, as amended (together with any statement) under Article 19

pages NONE, filed with the demand

pages NONE, filed with the letter of _____.

the drawings:

pages 1-4, as originally filed

pages NONE, filed with the demand

pages NONE, filed with the letter of _____.

the sequence listing part of the description:

pages NONE, as originally filed

pages NONE, filed with the demand

pages NONE, filed with the letter of _____.

2. With regard to the language, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item.

These elements were available or furnished to this Authority in the following language _____ which is:

the language of a translation furnished for the purposes of international search (under Rule 23.1(b)).

the language of publication of the international application (under Rule 48.3(b)).

the language of the translation furnished for the purposes of international preliminary examination (under Rules 55.2 and/or 55.3).

3. With regard to any nucleotide and/or amino acid sequence disclosed in the international application, the international preliminary examination was carried out on the basis of the sequence listing:

contained in the international application in printed form.

filed together with the international application in computer readable form.

furnished subsequently to this Authority in written form.

furnished subsequently to this Authority in computer readable form.

The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.

The statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished.

4. The amendments have resulted in the cancellation of:

the description, pages NONE

the claims, Nos. NONE

the drawings, sheets/fig NONE

5. This report has been established as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**

* Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rules 70.16 and 70.17).

** Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report.

V. Reasoned statement under Rule 66.2(a)(ii) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. STATEMENT

Novelty (N)	Claims <u>5, 8</u>	YES
	Claims <u>1-4, 6-7, 9</u>	NO
Inventive Step (IS)	Claims <u>5</u>	YES
	Claims <u>1-4, 6-9</u>	NO
Industrial Applicability (IA)	Claims <u>1-9</u>	YES
	Claims <u>NONE</u>	NO

2. CITATIONS AND EXPLANATIONS

Claims 1- 4, 6, 7, 9 meet novelty under PCT Article 33(2) as being anticipated by Eschauzier et al., U.S. Patent # 6,160,450.

Fig. 5 of Eschauzier et al. discloses a circuit comprising: transistors Qcos1, Qcos2, Q6, Q1, Q2 can be read as a circuit that computes a base current; transistors Q3, Q4 (wherein Q3, Q4 is a current mirror circuit), Qout, current Itail, resistor Roffset can be read as a circuit that generates an offset current; transistors M1 , M2 can be read as a BiCMOS differential amplifier; transistor Qout (which is an emitter follower circuit) and diode Dbias can be read as a buffering circuit.

Claim 8 lacks an inventive step under PCT Article 33(3) as being obvious over Eschauzier et al., U.S. Patent # 6,160,450.

Although the invention claims MOS transistors and Eschauzier uses bipolar transistors, it would have been obvious at the time the invention was made to a person having ordinary skill in the art to substitute MOS transistors for bipolar transistors because such substitution is a well known practice in the art in the absence of unexpected results since this is a matter of design choice.

Claim 5 meets the criteria set out in PCT Article 33(2)-(3), because the prior art does not teach or fairly suggest the alternating current filter has, in combination with other limitations, the emitter-follower circuit comprises at least two bipolar junction transistors.

Claims 1-9 meet the criteria set out in PCT Article 33(4), and thus have industrial applicability because the subject matter claimed can be made or used in industry.

----- NEW CITATIONS -----